# NONPROVISIONAL PATENT APPLICATION

### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Attorney Docket No.: 107082

August 18, 2000 Date:

### BOX PATENT APPLICATION

NONPROVISIONAL APPLICATION TRANSMITTAL **RULE §1.53(b)** 

Director of the U.S. Patent and Trademark Office Washington, D.C. 20231

Sir:

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Transmitted herewith for filing under 37 C.F.R. §1.53(b) is the nonprovisional patent application

For (Title):

PROCESSES FOR MANUFACTURING FLEXIBLE WIRING BOARDS AND THE

RESULTING FLEXIBLE WIRING BOARD

By (Inventors):

Hideyuki KURITA, Masanao WATANABE

| Ø | Formal | drawings | (Figs. | 1-8c; 7 | sheets) | are attached. |
|---|--------|----------|--------|---------|---------|---------------|
|---|--------|----------|--------|---------|---------|---------------|

A Declaration and Power of Attorney is filed herewith.

An assignment of the invention to SONY CHEMICAL CORP. is filed herewith.

An Information Disclosure Statement is filed herewith.

A statement to establish small entity status under 37 C.F.R. §§1.9 and 1.27 is filed herewith.

A Preliminary Amendment is filed herewith. 

Please amend the specification by inserting before the first line the sentence --This nonprovisional application claims the benefit of U.S. Provisional Application No. \_\_\_\_\_, filed

Priority of foreign application No. JP 11-239358 filed August 26, 1999 in Japan is claimed (35 U.S.C. §119).  $\boxtimes$ 

A certified copy of the above corresponding foreign application(s) is filed herewith.  $\boxtimes$ 

The filing fee is calculated below: Ø

# CLAIMS IN THE APPLICATION AFTER ENTRY OF ANY PRELIMINARY AMENDMENT NOTED ABOVE

| FOR:                                  | NO. FILED   | NO. EXTRA                 |  |  |
|---------------------------------------|---|---------------------------|--|--|
| BASIC FEE                             | 1. Sept. 1. | reservation of the second |  |  |
| TOTAL CLAIMS                          | 17 - 20   | = 0*                      |  |  |
| INDEP CLAIMS                          | 2 - 3   | = 0*                      |  |  |
| ☐ MULTIPLE DEPENDENT CLAIMS PRESENTED |   |                           |  |  |

<sup>\*</sup> If the difference is less than zero, enter "0".

# **SMALL ENTITY**

#### **RATE** FEE <u>OR</u> \$ 345 <u>OR</u> x9 =\$ OR x 39 =\$ \$ +130 =\$ TOTAL

OR

<u>OR</u>

<u>OR</u>

# OTHER THAN A **SMALL ENTITY**

| RATE  | FEE    |  |
|-------|--------|--|
| 100 P | \$ 690 |  |
| x 18  | \$     |  |
| x 78  | \$     |  |
| +260  | \$     |  |
| TOTAL | \$690  |  |

Check No. 111204 in the amount of \$690.00 to cover the filing fee is attached. Except as otherwise noted  $\boxtimes$ herein, the Director is hereby authorized to charge any other fees that may be required to complete this filing, or to credit any overpayment, to Deposit Account No. 15-0461. Two duplicate copies of this sheet are attached.

This application is entitled to small entity status. DO NOT charge large entity fees to our Deposit Account. 

Respectfully submitted,

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### Application Information

Title Line One:: PROCESSES FOR MANUFACTURING FLEXIBLE

Japan

Title Line Two:: WIRING BOARD AND THE RESULTING

FLEXIBLE WIRING BOARD Title Line Three::

Title Line Four::

Total Drawing Sheets:: 7

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# Continuity Information

>This application is a:: Application One::

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# Prior Foreign Applications

Foreign Application One::

Filing Date::

Country::

Priority Claimed::

Foreign Application Two::

Filing Date::

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Priority Claimed::

Foreign Application Three::

Filing Date::

Country::

Priority Claimed::

JP 11-239358

August 26, 1999

Japan yes

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# PROCESSES FOR MANUFACTURING FLEXIBLE WIRING BOARDS AND

# THE RESULTING FLEXIBLE WIRING BOARDS

### 5 FIELD OF THE INVENTION

The present invention relates to the field of flexible wiring boards, particularly the field of multilayer flexible wiring boards.

### 10 PRIOR ART

Double-sided flexible wiring boards having patterned metal films on both sides of a resin film are widely used because of the high degree of freedom of interconnection.

The metal films on both sides of the resin film are electrically connected to each other. Conventional methods for connecting these metal films are explained below.

First, the through hole method is explained. Referring to Fig. 7(a), the reference number 110 represents a base material for flexible wiring boards having metal films 112, 113 consisting of a copper foil adhered to the top surface and the bottom surface of a polyimide film 111.

This base material 110 is punched with a drill or the like to form a through hole 118 as shown in Fig. 7(b). Then, the assembly is carbonized and then electroplated, so that a copper plating layer 115 grows within the through hole 118 and on the surfaces of the

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metal films 112, 113 to connect the two metal films 112, 113 via the copper plating layer 115 within the through hole 118, as shown in Fig. 7(c).

Secondly, the via hole method is explained. Referring to Fig. 8(a), a base material 120 having a polyimide film 121 adhered on a metal film 122 consisting of a copper foil is prepared and an opening 128 is formed in the polyimide film 121 by photolithography (Fig. 8(b)).

Thus, the metal film 122 is exposed at the bottom of the opening 128, and a copper thin film is formed by sputtering on the surface of the metal film 122 exposed at the bottom of the opening 128 and on the surface of the polyimide film 121 in this state followed by electroplating to form a copper plating layer 123 on the top surface of the polyimide film 121 and the inner face of the opening 128 and the top of the metal film 122 exposed at the bottom of the opening 128. This copper plating layer 123 is connected to the metal film 122 at the bottom of the opening 128.

However, the through hole connection method requires a long time to form many through holes 118 because through holes 118 are drilled one by one. Moreover, it cannot satisfy the demand for fine patterns because the size of through holes 118 is limited to about 0.2 mm  $\phi$ .

The metal film 123 formed by the via hole method is easily separated because of the weak adhesive power between the copper plating layer 123 and the polyimide film 121. Furthermore, defects

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such as pinholes are more liable to occur in the copper plating layer 123 to make inferior the reliability.

An object of the present invention is to overcome the disadvantages of the prior art described above and to provide a technique that can connect metal films without forming any opening.

### SUMMARY OF THE INVENTION

In order to attain the above object, the present invention provides a process for manufacturing a flexible wiring board, comprising the steps of forming an uncured first resin film including a solvent on a first metal film, pressing bumps on a second metal film against said first resin film to force said bumps into said first resin film until the tops of said bumps come into contact with said first metal film, then patterning at least one of said first or second metal films, and heat-treating said first resin film while the top surface of said first resin film is at least partially exposed to cure said first resin film.

In one embodiment of the process for manufacturing a flexible wiring board according to the present invention, said uncured first resin film is semicured by heating it before said bumps are pressed against said first resin film.

In another embodiment of the process for manufacturing a flexible wiring board according to the present invention, said semicuring step takes place at a temperature lower than the boiling point of said solvent included in said uncured first resin film.

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In another embodiment of the process for manufacturing a flexible wiring board according to the present invention, said semicuring step takes place at a temperature ranged from 80  $^{\circ}$ C to 300  $^{\circ}$ C.

In another embodiment of the process for manufacturing a flexible wiring board according to the present invention, said first resin film is softened by heating it when said bumps are forced into said first resin film.

In another embodiment of the process for manufacturing a flexible wiring board according to the present invention, said curing step is followed by applying ultrasonic wave to either one or both of said bumps and said first metal film to connect said bumps to said first metal film.

In another embodiment of the process for manufacturing a flexible wiring board according to the present invention, said step of curing said first resin film is preceded by patterning either one of said first or second metal film and ultrasonic treating the unpatterned metal film and then patterning it.

Another embodiment of the process for manufacturing a flexible wiring board according to the present invention further comprises the steps of forming a second resin film on the top surface of said patterned first or second metal film, then pressing bumps on a third metal film against said second resin film to force said bumps into said second resin film until they come into contact with said first or second metal film, then patterning said third metal

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film and then curing said second resin film.

In this embodiment, said uncured first resin film may be semicured by heating it before said bumps are pressed against said first resin film.

Another embodiment of the process for manufacturing a flexible wiring board according to the present invention further comprises the steps of forming a second resin film on the top surface of said patterned first or second metal film, then pressing bumps on a third metal film against said second resin film to force said bumps into said second resin film until they come into contact with said first or second metal film, then patterning said third metal film, then curing said second resin film and then applying ultrasonic wave to said bumps on said third metal film to connect said bumps to said first or second metal film.

In this embodiment, said curing step may be followed by applying ultrasonic wave to said bumps on said third metal film to connect said bumps to said first or second metal film. It is possible to apply ultrasonic wave indirectly to said bumps by applying ultrasonic wave to said first or second metal film to connect said bumps to said first or second metal film. It is also possible to apply ultrasonic wave to both said bumps and said first or second metal film.

In this embodiment, said uncured first resin film may also be semicured by heating it before said bumps are pressed against said first resin film.

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The present invention also provides a flexible wiring board comprising a plurality of patterned metal films with a resin film being interposed therebetween among which adjacent two metal films are electrically connected to each other via bumps, wherein said resin film is cured after said bumps are pressed against the top surface of said resin film and forced into said resin film to electrically connect said two metal films via said bumps.

In one embodiment of the flexible wiring board according to the present invention, said resin film is cured by heat-treating it while the surface of said resin film is at least partially exposed between said patterned metal films.

In another embodiment of the flexible wiring board according to the present invention, one of said two adjacent metal films connected via said bumps is ultrasonically bonded to said bumps.

According to the present invention as defined above, bumps are pressed against a first resin film and forced into the first resin film. Thus, the bumps can be contacted with the metal film underlying the first resin film without forming any opening in the first resin film. The first resin film is preferably softened by heating it when the bumps are forced into the first resin film.

The bumps may be forced into the resin film by applying ultrasonic wave to the bumps digging or softening the semicured resin film in contact with the bumps.

When the bumps are embedded into the first resin heated, the first and second metal films are adhered to the first resin film.

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When at least one of the first and second metal films is patterned in this state to form an opening, the top of the first resin film is exposed at the bottom of the opening.

In this case, the top surface of the first resin film is partially covered with the first or second metal film and partially exposed. When the first resin film is heated in this state, the solvent and moisture included in the first resin film or the moisture generated during the progress of the chemical reaction caused by heating is discharged from the exposed first resin film so that the first resin film is cured. This curing step gives a double-sided flexible wiring board.

The curing step allows the first resin film to thermally shrink and the bumps to be strongly pressed against the first metal film, whereby the first and second metal films are electrically connected via the bumps.

In this case, ultrasonic wave may be applied to cause ultrasonic vibration interface between the bumps and the first metal film after the first resin film has been cured, so that the first metal film and the bumps are bonded by ultrasonic vibration energy. Ultrasonic wave may be applied on either side of the first metal film or the second metal film.

The height of the bumps used for connecting metal films is preferably greater than the thickness of the first resin film in which the bumps are to be embedded.

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### BRIEF DESCRIPTION OF THE DRAWINGS

- FIG. 1 (a)-(e) is a processing diagram showing a process for manufacturing a flexible wiring board according to the present invention (early steps).
- FIG. 2 (f)-(i) is a processing diagram showing a process for manufacturing a flexible wiring board according to the present invention (middle steps).
  - FIG. 3 (j)-(m) is a flow sheet showing a process for manufacturing a flexible wiring board according to the present invention (late steps).
  - FIG. 4 (a)-(e) is a flow sheet showing a process for manufacturing a flexible wiring board having a multilayer structure according to the present invention (the first half).
  - FIG. 5 (f)-(h) is a flow sheet showing a process for manufacturing a flexible wiring board having a multilayer structure according to the present invention (the second half).
  - FIG. 6 shows an example of ultrasonic welding apparatus used in the process according to the present invention.
- FIG. 7 (a)-(c) is a flow sheet showing the conventional through hole method.
  - FIG. 8 (a)-(c) is a flow sheet showing the conventional via hole method.

### DETAILED DESCRIPTION OF THE INVENTION

25 An example of flexible wiring board of the present invention

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and a process for manufacturing it will now be described.

First metal film will be described after-mentioned description.

Referring to Fig. 1(a), the reference number 11 represents a second metal film consisting of a rolled copper foil having a thickness of about 10  $\mu$ m - 20  $\mu$ m. A carrier film 32 and a photosensitive film 33 are adhered to the top surface and the bottom surface of the second metal film 11, respectively (Fig. 1(b)).

Then, the photosensitive film 33 is photographically patterned to form an opening 34 (Fig. 1(c)). The second metal film 11 is exposed at the bottom of this opening 34.

When the assembly is immersed into a plating solution for electroplating, copper is deposited on the second metal film 11 exposed at the bottom of the opening 34. Copper deposits fill the opening 34. The reference number 21 in Fig. 1(d) represents a bump formed from copper deposited in the opening 34.

Then, the carrier film 32 and the photosensitive film 33 are removed to expose the top surface and the bottom surface of the second metal film 11. Bumps 21 stand on the top surface of the second metal film 11 (Fig. 1(e)).

Separately from the second metal film 11, a first metal film 12 consisting of a rolled copper foil having a thickness of 9  $\mu$ m - 35  $\mu$ m is prepared (Fig. 2(f)), and a polyimide precursor solution is applied on its surface and heat-treated to form a first resin film 16 consisting of a polyimide film (Fig. 2(g)).

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The heat treatment temperature is lower than the boiling point of the solvent for the polyimide precursor solution. The heat treatment was carried out at a temperature (150 °C - 200 °C) lower than the boiling point 202 °C of the solvent N-methyl pyrrolidone for the polyimide precursor solution used here. The first resin film 16 in this state has been only slightly imidated and semicured.

Although the first resin film 16 here was formed by applying a polyimide precursor solution and heat-treating it, a preliminarily semicured resin film such as polyimide may be adhered onto the first metal film 12 to form the first resin film 16.

Then, the bumps 21 on the second metal film 11 treated as above are faced to the first resin film 16 on the first metal film 12 as shown in Fig. 2(h) and hot-pressed to force the bumps 21 into the first resin film 16.

Fig. 2(i) shows that the bumps 21 have been forced into the resin film 16 so that they are in contact with the first metal film 12 underlying the first resin film 16.

Hot-pressing softens the first resin film 16 to help the bumps 21 to be forced into it and induces adhesion on the surface of the first resin film 16 to adhere the second metal film 11 to the first resin film 16.

The hot-pressing conditions here are 50 kg/cm $^2$  at 150  $^{\circ}$ C and the hot-pressing period is about 10 minutes.

Then, a patterned resist layer is formed on the top surface

25 of the first metal film 12 and etched to pattern the first metal

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film 12. After etching, the resist layer is removed to give a flexible wiring board 3 having the patterned first metal film 12 (Fig. 3(j)). The reference number 35 in Fig. 3(j) represents a opening formed removal zone of the patterned first metal film 12. The opening 35 is a zone dividing wiring from each other. The first resin film 16 is exposed at the bottom of the opening 35. However, the first resin film 16 is not exposed at the bottom surface of the first resin film 16 on the side of the second metal film 11.

When this flexible wiring board 3 is heat-treated at a temperature of 160 °C - 350 °C in a baking apparatus for several hours, the residual solvent included in the first resin film 16 is discharged into the atmosphere from the exposed first resin film 16 at the bottom of the opening 35 in the patterned first metal film 12. This heat treatment degases the first resin film 16 and promotes imidation reaction within the first resin film 16 to cure the first resin film 16. The moisture generated during imidation reaction is discharged from the exposed first resin film 16 by heat treatment.

Once the first resin film 16 is cured by this imidation reaction, the first and second metal films 12, 11 are fixed to the first resin film 16. During then, the first resin film 16 thermally shrinks and the bumps 21 are pressed against the first metal film 12, whereby the first and second metal films 12, 11 are electrically connected via the bumps 21.

Then, the bumps 21 and the first metal film 12 are

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ultrasonically bonded to enhance the reliability of their electric connection.

The reference number 50 in Fig. 6 represents an ultrasonic bonding apparatus used for this ultrasonic bonding.

This ultrasonic bonding apparatus 50 comprises a platform 56, two guide posts  $57_1$ ,  $57_2$  upright on the platform 56, an ultrasonic wave generator 51 supported to be vertically movable by the guide posts  $57_1$ ,  $57_2$ , and a resonator 52 attached to an end of the ultrasonic wave generator 51.

A working table 58 is placed on the platform 56 and a flexible wiring board 3 imidated as described above is mounted on the top of the working table 58.

When a planer tip 54 of the resonator 52 is positioned in parallel to the surface of the working table 58 and an air cylinder 53 of the ultrasonic bonding apparatus 50 is activated so that the ultrasonic wave generator 51 and the resonator 52 vertically descend along the guide posts  $57_1$ ,  $57_2$ . The tip 54 of the resonator 52 comes into close contact with the flexible wiring board 3.

This state is shown in Fig. 3(k), in which the tip 54 of the resonator 52 is pressed against the flexible wiring board 3 by the air cylinder 53 so that the tops of the bumps 21 are strongly pressed against the first metal film 12 because the first resin film 16 is softer than the first and second metal films 12, 11 and the bumps 21.

When the ultrasonic wave generator 51 is activated in this

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state to apply ultrasonic wave to the resonator 52, the ultrasonic wave resonates within the resonator 52 so that the tip 54 of the resonator 52 ultrasonically vibrates. This ultrasonic vibration causes rubbing interface between the first metal film 12 and the bumps 21, whereby the tops of the bumps 21 are metallically bonded to the first metal film 12. In this case, preliminary solder plating on the bumps 21 further facilitates bonding.

The flexible wiring board 3 is removed from the ultrasonic bonding apparatus 50 and a patterned resist layer is formed on the top surface of the second metal film 11, which is then etched. After etching, the resist layer is removed. The reference number 36 in Fig. 3(1) represents an opening formed in the patterned second metal film 11.

The flexible wiring board 4 having the patterned second metal film 11 has the first and second metal films 12, 11 on the top and the bottom of the first resin film 16, and the first resin film 16 is exposed at the bottoms of the openings 35, 36 of the first and second metal films 12, 11.

An overcoat solution is applied on the surfaces of the first and second metal films 12, 11 of this flexible wiring board 4 and polymerized into film to form overcoat layers 25, 26, whereby a double-sided flexible wiring board 5 is obtained. Other electronic components can be connected to the first and second metal films 12, 11 exposed from openings not shown formed in predetermined zones of the overcoat layers 25, 26.

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Although overcoat layers 25, 26 may be formed to prepare a double-sided flexible wiring board 5, the flexible wiring board 4 having the first and second metal films 12, 11 exposed can also be used to prepare a flexible wiring board having a multilayer structure.

Fig. 4(a) shows a flexible wiring board 4 having the first and second metal films 12, 11 exposed on the top surface and the bottom surface of the first resin film 16. (This flexible wiring board 4 is the flexible wiring board 4 shown in Fig. 3(1).) A polyimide precursor solution is applied on the top surface of the flexible wiring board 4 and heat-treated to form a second resin film 18 consisting of a polyimide film shown by the reference number 18 in Fig. 4(b). This second resin film 18 has not been imidated.

A third metal film 13 having bumps 22 is prepared, and the bumps 22 are faced to the second resin film 18 (Fig. 4(c)) and brought into contact with the second resin film 18 and hot-pressed, whereby the bumps 22 are forced into the second resin film 18 until the tops of the bumps 22 come into contact with the patterned first metal film 12. During then, the third metal film 13 is bonded to the second resin film 18.

Then, a patterned resist layer is formed on the top surface of the third metal film 13, which is then patterned by etching.

The reference number 37 in Fig. 4(e) represents an opening in the patterned third metal film 13. The assembly is heat-treated while the top surface of the second resin film 18 is exposed at

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the bottom of this opening 37 under the same conditions as above to discharge the solvent and moisture from the opening 37 and thus imidate the second resin film 18.

This imidation allows the third metal film 13 to be fixed to the second resin film 18, which thermally shrink to press the bumps 22 against the first metal film 12, whereby the first and third metal films 12, 13 are electrically connected via the bumps 22. Thus, the first to third metal films 12, 11, 13 are electrically connected via the bumps 21, 22.

Then, the flexible wiring board 6 in this state is mounted on the working table 58 in the ultrasonic bonding apparatus 50 shown in Fig. 6 and brought into contact with the tip 54 of the resonator 52. When ultrasonic wave is applied, the bumps 22 ultrasonically vibrate and are ultrasonically bonded to the first metal film 12 in contact with them. After ultrasonic bonding, the flexible wiring board removed from the ultrasonic bonding apparatus 50 has a multilayer structure shown by the reference number 7 in Fig. 5(g).

A polyimide precursor may be applied on this flexible wiring board 7 to form a resin film, which may be further layered on a metal film having bumps and imidated. In this case, the steps shown in Fig. 4(b)-(e) and Fig. 5(f) are repeated.

An overcoat solution may be applied on the top and the bottom of this flexible wiring board 7 and cured to form overcoat layers 27, 28 on the second and third metal films 11, 13.

As has been described above, the present invention can

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simplify the process because patterned metal wiring films can be connected via bumps without preliminarily providing openings in the polyimide film.

The present invention also improves reliability because bumps and patterned metal wiring films are steadily electrically connected using an ultrasonic bonding apparatus.

Although the multilayer flexible wiring board 8 was formed by two steps of ultrasonic bonding according to the foregoing embodiments, the first to third metal films 11, 12, 13 may also be connected via bumps 21, 22 by a single application of ultrasonic wave.

Solder coat or gold coat may be formed on the bump tops to facilitate ultrasonic bonding.

Although the polyimide precursor solution used for forming the first or second resin film 16, 18 included N-methyl pyrrolidone as a solvent in the foregoing embodiments, polyimide precursor solutions including other solvents such as formalin or N-methylamide may also be used.

Instead of polyimide precursor solutions, liquid raw materials of other resins may also be used, such as liquid raw materials of modified epoxy resins, liquid raw materials of polyester resins or liquid raw materials of polyethylene resins.

When polyester liquid raw materials or the like include an organic solvent, a semicuring step can take place by heating to a temperature at or below the boiling point of the organic solvent.

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Alternatively, a semicured resin film such as a modified epoxy resin, polyester resin or polyethylene resin may be used.

Even if a non-polyimide resin liquid raw material is used to form first and second resin films 16, 18 or a semicured resin film is adhered to form first and second resin films 16, 18, it is also preferable to discharge either one of the organic solvent or moisture or both from the partially exposed resin film surfaces when the resin films are cured by heating after the bumps are forced into the semicured resin films.

In brief, the present invention widely includes processes for manufacturing a flexible wiring board, comprising the steps of pressing bumps into a uncured or semicured resin prior to a curing step to connect metal films on the top surface and the bottom surface of the uncured or semicured resin film via the bumps, and heat-treating the partially exposed resin film to cure it.

Although the metal films described above consisted of copper, other metals may also be used. Gold or other plating coats may be formed on metal films.

Although the resin film 16 was cured before the second metal film 11 was patterned in the foregoing embodiments, the first resin film 16 may also be cured after the first and second metal films 12, 11 have been patterned.

On the contrary, the first metal film 12 may be patterned and then the bumps 21 on the second metal film 11 may be pressed into the first resin film 16 on the top of the first metal film

12 to cure the first resin film 16 in this state. In brief, metal films in contact with bumps may be in the form of a metal foil or a patterned wiring film.

The process can be simplified by contacting bumps with a metal film underlying a resin film without forming any opening.

Moreover, electric reliability can be improved by applying ultrasonic wave to bumps and metal films in contact with the bumps to connect them by ultrasonic vibration energy.

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### WHAT IS CLAIMED IS:

- 1. A process for manufacturing a flexible wiring board, comprising the steps of forming an uncured first resin film including a solvent on a first metal film, pressing bumps on a second metal film against said first resin film to force said bumps into said first resin film until the tops of said bumps come into contact with said first metal film, then patterning at least one of said first or second metal films, and heat-treating said first resin film while the surface of said first resin film is at least partially exposed to cure said first resin film.
- 2. The process for manufacturing a flexible wiring board according to claim 1 wherein said uncured first resin film is semicured by heating it before said bumps are pressed against said first resin film.
- 3. The process for manufacturing a flexible wiring board according to claim 2 wherein said semicuring step takes place at a temperature lower than the boiling point of said solvent included in said uncured first resin film.
- 4. The process for manufacturing a flexible wiring board according to claim 2 wherein said semicuring step takes place at a temperature from 80 °C to 300 °C.

5. The process for manufacturing a flexible wiring board according to claim 2 wherein said first resin film is softened by heating it when said bumps are forced into said first resin film.

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6. The process for manufacturing a flexible wiring board according to claim 1 wherein said curing step is followed by ultrasonic treating to apply ultrasonic wave to either one or both of said bumps and said first metal film to connect said bumps to said first metal film.

- 7. The process for manufacturing a flexible wiring board according to claim 2 wherein said curing step is followed by ultrasonic treating to apply ultrasonic wave to either one or both of said bumps and said first metal film to connect said bumps to said first metal film.
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- 8. The process for manufacturing a flexible wiring board according to claim 6 wherein said step of curing said first resin film is preceded by patterning either one of said first or second metal film and patterning the unpatterned metal film after said ultrasonic treating.

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9. The process for manufacturing a flexible wiring board according to claim 7 wherein said step of curing said first resin

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film is preceded by patterning either one of said first or second metal film and patterning the unpatterned metal film after said ultrasonic treating.

- 10. The process for manufacturing a flexible wiring board according to claim 1 further comprising the steps of forming a second resin film on the top surface of said patterned first or second metal film, then pressing bumps on a third metal film against said second resin film to force said bumps into said second resin film until they come into contact with said first or second metal film, then patterning said third metal film and then curing said second resin film.
- 11. The process for manufacturing a flexible wiring board according to claim 2 further comprising the steps of forming a second resin film on the top of said patterned first or second metal film, then pressing bumps on a third metal film against said second resin film to force said bumps into said second resin film until they come into contact with said first or second metal film, then patterning said third metal film and then curing said second resin film.
- 12. The process for manufacturing a flexible wiring board according to claim 8 further comprising the steps of forming a second resin film on the top of said patterned first or second metal film,

then pressing bumps on a third metal film against said second resin film to force said bumps into said second resin film until they come into contact with said first or second metal film, then patterning said third metal film, then curing said second resin film and then applying ultrasonic wave to said bumps on said third metal film to connect said bumps to said first or second metal film.

- 13. The process for manufacturing a flexible wiring board according to claim 9 further comprising the steps of forming a second resin film on the top of said patterned first or second metal film, then pressing bumps on a third metal film against said second resin film to force said bumps into said second resin film until they come into contact with said first or second metal film, then patterning said third metal film, then curing said second resin film and then applying ultrasonic wave to said bumps on said third metal film to connect said bumps to said first or second metal film.
- 14. A flexible wiring board comprising a plurality of patterned metal films with a resin film being interposed therebetween among which adjacent two metal films are electrically connected to each other via bumps, wherein said resin film is cured after said bumps are pressed against the top surface of said resin film, and forced into said resin film to electrically connect said two metal films via said bumps.

15. The flexible wiring board according to claim 14 wherein said resin film is cured by heat-treating it while the top surface of said resin film is at least partially exposed between said patterned metal films.

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- 16. The flexible wiring board according to claim 14 wherein one of said two adjacent metal films connected via said bumps is ultrasonically bonded to said bumps.
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- 17. The flexible wiring board according to claim 15 wherein one of said two adjacent metal films connected via said bumps is ultrasonically bonded to said bumps.

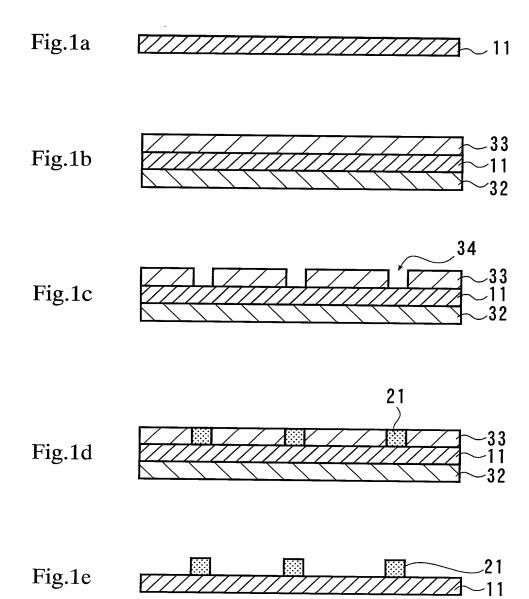
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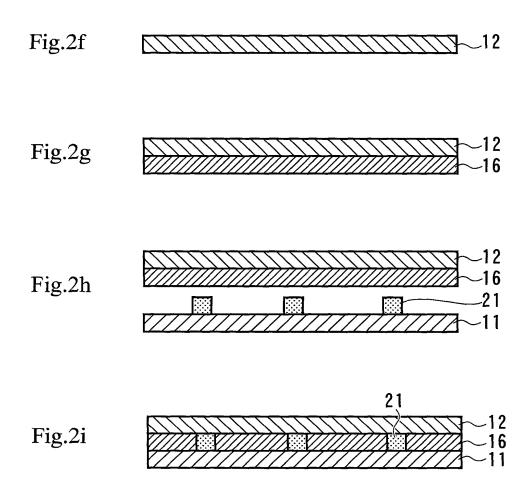
### ABSTRACT

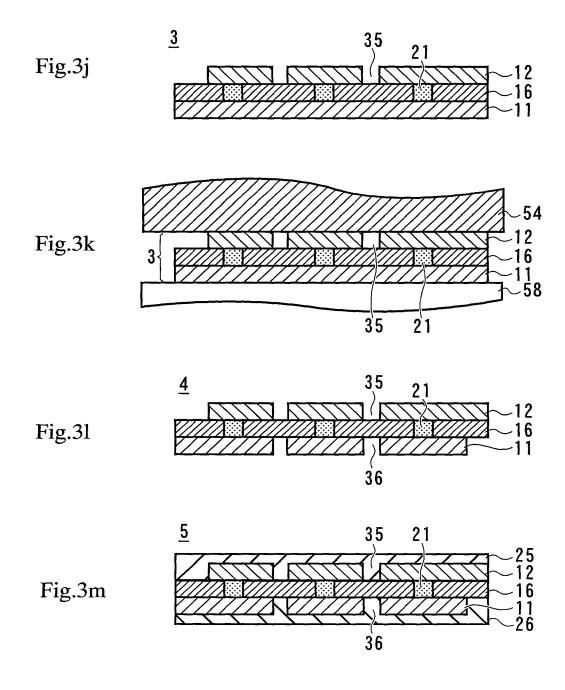
The present invention aims to connect metal films without forming any opening in a resin film.

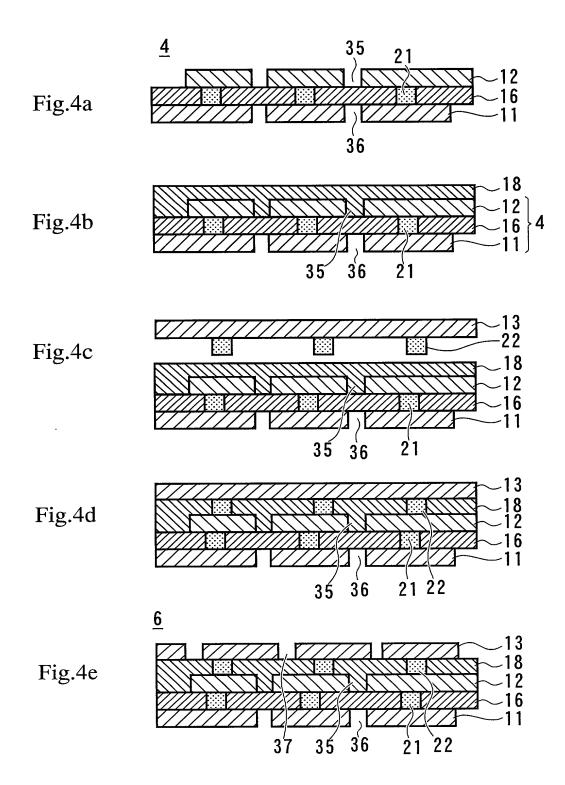
Against a first resin film 16 formed on a first metal film 12 are pressed bumps 21 on a second metal film 11 so that the bumps 21 are embedded into the first resin film 16. Either one of the first metal film 12 or the second metal film 11 or both is (are) patterned while the bumps 21 are in contact with the first metal film 12, and the first resin film 16 is heat-treated while the top of the first resin film is partially exposed to discharge the solvent or moisture from the exposed zone and cure the first resin film 16. After curing, the bumps 21 and the first metal film 12 may be ultrasonically bonded to each other. A second resin film and a third metal film may be further layered to form a multilayer structure.

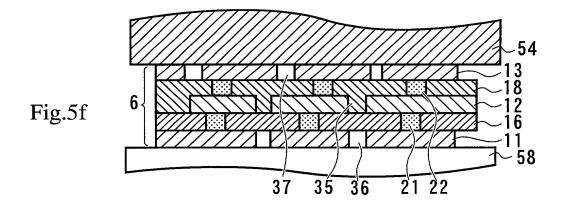
24

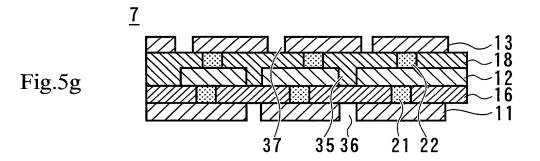












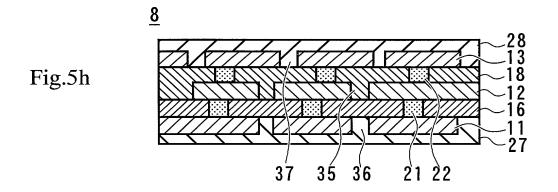
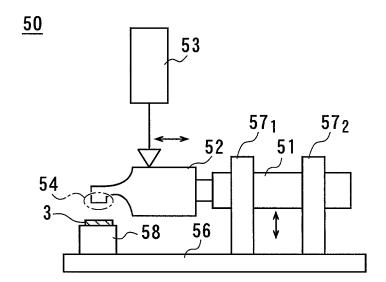
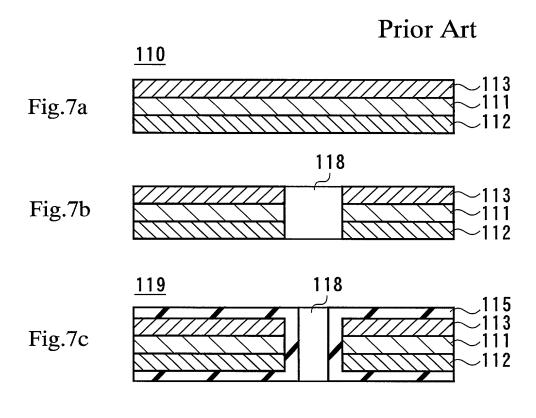
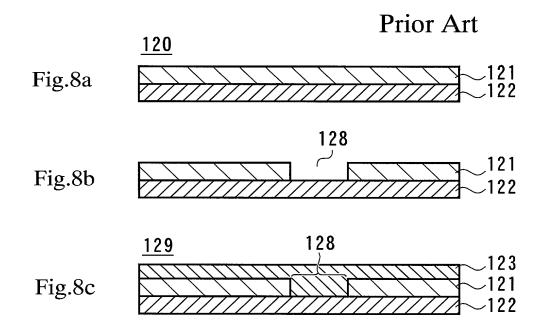


Fig.6







2

# APPLICATION FOR UNITED STATES PATENT DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name; that

I verily believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural inventors are named below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

PROCESSES FOR MANUFACTURING FLEXIBLE WIRING BOARD AND THE RESULTING FLEXIBLE WIRING BOARD described and claimed in the specification:

| Check one |   |                  |
|-----------|---|------------------|
| *a.       | attached hereto.                          |                  |
| b.        | filed on as Application No and amended on | (if applicable). |

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose to the Office all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, §1.56.

Under Title 35, U.S. Code §119, the priority benefits of the following foreign application(s) and/or United States provisional application(s) filed within one year prior to this application are hereby claimed:

Japanese Patent Application No.11-239358 filed August 26, 1999

The following application(s) for patent or inventor's certificate on this invention were filed in countries foreign to the United States of America either (a) more than one year prior to this application, or (b) before the filing date of the above-named foreign priority application(s) and/or United States provisional application(s):

I hereby appoint the following as my attorneys of record with full power of substitution and revocation to prosecute this application and to transact all business in the Patent Office:

James A. Oliff, Reg. No. 27,075; William P. Berridge, Reg. No. 30,024; Kirk M. Hudson, Reg. No. 27,562; Thomas J. Pardini, Reg. No. 30,411; Edward P. Walker, Reg. No. 31,450; Robert A. Miller, Reg. No. 32,771 and Mario A. Costantino, Reg. No. 33,565.

ALL CORRESPONDENCE IN CONNECTION WITH THIS APPLICATION SHOULD BE SENT TO OLIFF & BERRID GE, PLC, P.O. BOX 19928, ALEXANDRIA, VIRGINIA 22320, TELEPHONE (703) 836-6400.

I hereby declare that I have reviewed and understand the contents of this Declaration, and that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

| Typewritten Full N<br>of First or Sole In                              |                                      | HIDEYUKI                       |  | KURITA             |  |
|--|--------------------------------------|--------------------------------|--|--------------------|--|
| **Inventor's Signat  | rure:                                | Given Name  History            | Middle Initial   | Family Name        |  |
| **Date of Signature  | e:<br>                               | 5                              | 16   | 2000               |  |
| Residence:   | TOCH                                 | Month<br>IIGI                  | Day<br>JA  | <b>Year</b><br>PAN |  |
| Citizenship:   |                                      | City<br>NESE                   | State or Province  | Country            |  |
|  | Post Office Address (Insert complete |                                | ls Corp. Dai-2 Factory   |                    |  |
| mailing address, including country) 12-3, Satsuki-cho Kanuma-shi, Toch |                                      |                                | o Kanuma-shi, Tochigi :  | 322–8502 Japan     |  |
| *If Box (a.) is ch<br>**Note to Invent                                 | or: Please sign name exac            | tly as it appears above and in | to the specification (including claims) sert actual date of signing. | ,                  |  |

# (Discard this page in a sole inventor application)

| 1 | Typewritten Full Name  |            |                      |                  |
|---|--|------------|----------------------|------------------|
|   | of Joint Inventor  | MASANAO    |                      | WATANABE         |
| • | Towns to all Cime towns  | Given Name | Middle Initial       | Family Name      |
| 2 | Inventor's Signature:  | masanao    |                      | Walanabe         |
| 3 | Date of Signature:   | 5          | /6                   | 2000             |
|   | Residence: TOCHTO  | Month      | Day                  | Year             |
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|   | Citizenship: JAPANI  | •          | State of Trovince    | Country          |
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| 2 | Inventor's Signature:  | Given Name | Middle Initial       | Family Name      |
| 3 | Date of Signature:   |            |                      |                  |
|   | Residence:   | Month      | Day                  | Year             |
|   | Citizenship:   | City       | State or Province    | Country          |
|   | Post Office Address:<br>(Insert complete mailing address, including country) |            |                      |                  |
| 1 | Typewritten Full Name of Joint Inventor                                      |            |                      |                  |
| 2 | Inventor's Signature:  | Given Name | Middle Initial       | Family Name      |
| 3 | Date of Signature:   | Month      | Dev                  | Year             |
|   | Residence:   |            | Day                  |                  |
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| 1 | Typewritten Full Name of Joint Inventor                                      |            |                      |                  |
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| 3 | Date of Signature:   |            |                      |                  |
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|   | Citizenship:   | City       | State or Province    | Country          |
|   | Post Office Address:   |            |                      |                  |
|   | (Insert complete mailing address, including country)                         |            |                      |                  |

Note to Inventor: Please sign name on line 2 exactly as it appears in line 1 and insert the actual date of signing on line 3.

This form may be executed only when attached to the first page of the Declaration and Power of Attorney of the application to which it pertains.